

IN THE SPECIFICATION

Please amend the replacement specification at page 7, line 29 through page 8, line 7 as shown below.

The first storage circuitry has an input 32 for receiving the variables. The output 34 of the first storage circuitry is coupled to an input 42 of a processor 40. The processor receives the variables via the output 34 of the first storage circuitry and ~~consults~~ constructs a function graph by arranging the variables in a representation of the nodes of the graph such that the nodes are labeled with the variables so that the set of functions labeling leaves reachable from a node corresponds to the set of functions which depend on the variables labeling the node, as shown in Figure 6. The processor then traverses the graph in a depth-first manner, as indicated in Figure 6 by the arrow, to construct a list of the labels in a selected order. The list is output from the processor via output 44 which is coupled the input 52 of second storage circuitry.